

ABSTRACT OF THE DISCLOSURE

A demodulation circuit and a demodulation method can optimize sampling timing with achieving reduction of power consumption. A preliminarily known signal is inserted in the digital transmission signal upon transmission. The demodulation circuit includes A/D converting means for performing A/D conversion of a base band signal obtained by demodulation of the digital transmission signal, and phase shifting means for causing phase shift of one of the digital transmission signal and the base band signal on the basis of the known signal after digital conversion by the A/D converting means and the known signal upon transmission.